



INFORMATION DISCLOSURE STATEMENT

Applicant : Bulent M. Basol
App. No : 10/705,360
Filed : November 10, 2003
For : ELECTROCHEMICAL
MECHANICAL PROCESSING USING
LOW TEMPERATURE PROCESS
ENVIRONMENT
Examiner : Harry D. Wilkins, III
Art Unit : 1742

CERTIFICATE OF MAILING

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as first-class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on

September 15, 2006

(Date)

Tina Chen, Reg. No. 44,606

Mail Stop Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is a PTO/SB/08 Equivalent listing 68 references to be considered by the Examiner. Also enclosed are 10 foreign patent references and/or non-patent literature as listed on the Information Disclosure Statement.

This Information Disclosure Statement is being filed before the mailing date of a final action and before the mailing of a Notice of Allowance. This Statement is accompanied by the fees set forth in 37 C.F.R. § 1.17(p). The Commissioner is hereby authorized to charge any additional fees which may be required or to credit any overpayment to Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 9/15/06

By: 

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Multiple sheets used when necessary)	Application No.	10/705,360
	Filing Date	November 10, 2003
	First Named Inventor	Bulent M. Basol
	Art Unit	1742
SHEET 1 OF 3	Examiner	Harry D. Wilkins, III
	Attorney Docket No.	ASMNUT.027DVCP1

U.S. PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number Number - Kind Code (if known) Example: 1,234,567 B1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
	1	2,965,556	12/1960	Gudmund Damgaard Laurits	
	2	3,367,468	01/1972	Icxi et al.	
	3	3,448,023	06/1969	Bell Ramsay M	
	4	3,595,089	05/1976	Watts	
	5	3,779,887	12/1973	Gildone	
	6	3,959,089	05/1976	Watts	
	7	4,024,029	05/17/1977	Rain et al.	
	8	4,391,684	07/1983	Goddard	
	9	4,431,5001	02/1984	Leppanen	
	10	4,466,864	08/1984	Bacon et al.	
	11	4,610,772	09/1986	Palnik	
	12	5,024,735	06/1991	Kadija	
	13	5,171,412	12/1992	Talieh et al.	
	14	5,429,733	07/1995	Ishida	
	15	5,558,568	09/1996	Talieh et al.	
	16	5,567,300	10/26/1996	Datta et al.	
	17	5,692,947	12/1997	Talieh et al.	
	18	5,700,366	12-1997	Steblianko et al.	
	19	5,755,859	05/1998	Brusic et al.	
	20	5,807,165	09/15/1998	Uzoh et al.	
	21	5,816,900	10/1998	Nagahara et al.	
	22	5,833,820	11/1998	Dublin	
	23	5,862,605	01/26/1999	Horie et al.	
	24	5,863,412	01/1999	Ichinose et al.	
	25	5,911,619	06/1999	Uzoh et al.	
	26	5,930,669	07/1999	Uzoh	
	27	5,933,753	08/1999	Simon et al.	
	28	6,004,880	12/1999	Liu et al.	
	29	6,142,155	11/2000	Adams et al.	

Examiner Signature

Date Considered

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

T¹ - Place a check mark in this area when an English language Translation is attached.

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SHEET 2 OF 3	Attorney Docket No.	ASMNUT.027DVCP1

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.	Document Number Number - Kind Code (if known) Example: 1,234,567 B1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
	30	6,074,546	06/13/2000	Sun et al.	
	31	6,176,992 B1	01/23/2001	Talieh	
	32	6,210,554	04/2001	Kosaki et al.	
	33	6,251,235 B1	06/26/2001	Talieh et al.	
	34	6,270,647 B1	08/07/2001	Graham et al.	
	35	6,346,479 B1	02/12/2002	Woo et al.	
	36	6,352,623 B1	03/05/2002	Munks et al.	
	37	6,375,823	04/2002	Matsuda et al.	
	38	2002/0074238	06/2002	Mayer et al.	
	39	6,402,925 B2	06/11/2002	Talieh	
	40	6,413,388	07/2002	Uzoh et al.	
	41	6,440,295	08/2002	Wang, Hui	
	42	6,471,847 B2	10/29/2002	Talieh et al.	
	43	6,482,656 B1	11/19/2002	Lopatin	
	44	6,497,800 B1	12/24/2002	Talieh et al.	
	45	6,506,103 B1	01/14/2003	Ohmori et al.	
	46	2003/0054729 A1	03/20/2003	Lee et al.	
	47	6,600,229 B2	07/29/2003	Mukherjee et al.	
	48	6,610,190 B2	08/26/2003	Basol et al.	
	49	6,653,226 B1	11/25/2003	Reid	
	50	2003/00226764	12/2003	Moore et al.	
	51	6,676,822 B1	01/13/2004	Talieh	
	52	6,833,063 B2	12/21/2004	Basol	
	53	6,848,970 B2	02/01/2005	Manens et al.	
	54	6,867,136 B2	03/15/2005	Basol et al.	
	55	6,902,659 B2	06/07/2005	Talieh	
	56	6,936,154 B2	08/30/2005	Basol et al.	
	57	6,942,780 B2	09/13/2005	Basol et al.	
	58	6,958,114 B2	10/25/2005	Talieh et al.	

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SHEET 3 OF 3	Attorney Docket No.	ASMNUT.027DVCP1

FOREIGN PATENT DOCUMENTS						
Examiner Initials	Cite No.	Foreign Patent Document Country Code-Number-Kind Code Example: JP 1234567 A1	Publication Date MM-DD-YYYY	Name of Patentee or Applicant	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear	T ¹
	59	DE 2008664	09/1971			
	60	DE 4324330	03/1994			
	61	JP 2000208443 A	07/28/2000	Sony Corp.		
	62	WO 01/32362 A1	05/10/2001	Philips Semiconductors Inc.		

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ¹	
	63	CONTOLINI et al., "Electrochemical Planarization for Multilevel Metallization," September 1994, pp. 2503-2510.		
	64	MADORE et al. "Blocking Inhibitors in Cathodic Leveling," <i>I. Theoretical Analysis</i> , December 1996, pp. 3927-3942.		
	65	REID, et al., "Factors influencing damascene feature fill using copper PVD and electroplating," <i>Solid State Technology</i> , July 2000, 7 pages.		
	66	RUBINSTEIN et al. "Tampongalvanisieren in der Praxis, Teil 1," <i>Galvanotechnik</i> , Vol. 79, No. 10, 1998, pp. 3263-3270.		
	67	STEIGERWALD et al., "Pattern Geometry Effects in the Chemical-Mechanical Polishing of Inlaid Copper Structures," October 1994, pp. 2842-2848.		
	68	WEST et al., "Pulse Reverse Copper Electrodeposition in High Aspect Ratio Trenches and Vias," September 1998, pp. 3070-3073.		

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